

AMENDMENTS TO THE CLAIMS:

Please cancel claim 1, without prejudice. Kindly amend claim 2, as shown below.

This listing of claims will replace all prior versions and listings of claims in the
Application:

Claims 1 (cancelled).

Claim 2 (currently amended): A drive circuit of a plasma display panel ~~according to claim 1,~~
having scan electrodes and sustain electrodes that realizes display by discharge that is generated
by shifting potential between said scan electrodes and said sustain electrodes, comprising:

a first sustain driver circuit for both controlling potential on said scan electrode side and
effecting control such that, when said scan electrode side is at the power-supply potential, this
potential is used to raise the potential of said sustain electrode side;

a second sustain driver circuit for both controlling potential on said sustain electrode side
and effecting control such that, when said sustain electrode side is at the power-supply potential,
this potential is used to raise the potential of said scan electrode side;

a control circuit that controls the operation of said first and second sustain driver circuits
to repeat shifting of the potential between said scan electrodes and said sustain electrodes; and

a junction means for connecting said first sustain driver circuit and said second sustain
driver circuit,

wherein said first sustain driver circuit comprises:

a first switching element for clamping said scan electrodes to the power-supply potential;

a second switching element for clamping said scan electrodes to the ground potential;

a third switching element for both lowering the potential of said scan electrode side and
raising the potential of said sustain electrode side by causing current to flow from said first

sustain driver circuit to said second sustain driver circuit when said scan electrode side is at the power-supply potential; and

a first coil that is connected between said third switching element and said second sustain driver circuit;

and wherein said second sustain driver circuit comprises:

a fourth switching element for clamping said sustain electrodes to the power-supply potential;

a fifth switching element for clamping said sustain electrodes to the ground potential;

a sixth switching element for both lowering the potential of said sustain electrode side and raising the potential of said scan electrode side by causing a current to flow from said second sustain driver circuit to said first sustain driver circuit when said sustain electrode side is at the power-supply potential; and

a second coil that is connected between said sixth switching element and said first sustain driver circuit.

Claim 3 (original): A drive circuit of a plasma display panel according to claim 2 wherein:

said first sustain driver circuit includes a first diode that regulates the direction of current that flows between said first sustain driver circuit and said second sustain driver circuit to only the direction from said second sustain driver circuit to said first sustain driver circuit; and

said second sustain driver circuit includes a second diode that regulates the direction of current that flows between said second sustain driver circuit and said first sustain driver circuit to only the direction from said first sustain driver circuit to said second sustain driver circuit.

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Claim 4 (original): A drive circuit of a plasma display panel according to claim 3 wherein said first sustain driver circuit includes a third diode for preventing current from flowing to the power-supply voltage side by way of said first switching element.

Claim 5 (original): A drive circuit of a plasma display panel according to claim 3 wherein said second sustain driver circuit includes a fourth diode for preventing current from flowing to the power-supply voltage side by way of said fourth switching element.

Claim 6 (original): A drive circuit of a plasma display panel according to claim 3 wherein said first and second sustain driver circuits each includes a group of clamp diodes for absorbing spike voltage that is caused by counter-electromotive force in inductance that is present in said first and second coils as well as in said junction means.

Claim 7 (original): A drive circuit of a plasma display panel according to claim 4 wherein said first and second sustain driver circuits each includes a group of clamp diodes for absorbing spike voltage that is caused by counter-electromotive force in inductance that is present in said first and second coils as well as in said junction means.

Claim 8 (original): A drive circuit of a plasma display panel according to claim 5 wherein said first and second sustain driver circuits each includes a group of clamp diodes for absorbing spike voltage that is caused by counter-electromotive force in inductance that is present in said first and second coils as well as in said junction means.

Claim 9 (original): A drive circuit of a plasma display panel according to claim 6 wherein:

said first diode is provided on the side of said first and second switching elements from said clamp diode group; and

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said second diode is provided on the side of said fourth and fifth switching elements from said clamp diode group.

Claim 10 (original): A drive circuit of a plasma display panel according to claim 7 wherein:

said first diode is provided on the side of said first and second switching elements from said clamp diode group; and

said second diode is provided on the side of said fourth and fifth switching elements from said clamp diode group.

Claim 11 (original): A drive circuit of a plasma display panel according to claim 8 wherein:

said first diode is provided on the side of said first and second switching elements from said clamp diode group; and

said second diode is provided on the side of said fourth and fifth switching elements from said clamp diode group.

Claim 12 (original): A drive circuit of a plasma display panel according to claim 2 wherein said switching elements are FET transistors.

Claim 13 (original): A drive circuit of a plasma display panel according to claim 3 wherein said switching elements are FET transistors.

Claim 14 (original): A drive circuit of a plasma display panel according to claim 4 wherein said switching elements are FET transistors.

Claim 15 (original): A drive circuit of a plasma display panel according to claim 5 wherein said switching elements are FET transistors.

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Claim 16 (original): A drive circuit of a plasma display panel according to claim 6 wherein said switching elements are FET transistors.

Claim 17 (original): A drive circuit of a plasma display panel according to claim 7 wherein said switching elements are FET transistors.

Claim 18 (original): A drive circuit of a plasma display panel according to claim 8 wherein said switching elements are FET transistors.

Claim 19 (original): A drive circuit of a plasma display panel according to claim 9 wherein said switching elements are FET transistors.

Claim 20 (original): A drive circuit of a plasma display panel according to claim 10 wherein said switching elements are FET transistors.

Claim 21 (original): A drive circuit of a plasma display panel according to claim 11 wherein said switching elements are FET transistors.

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